

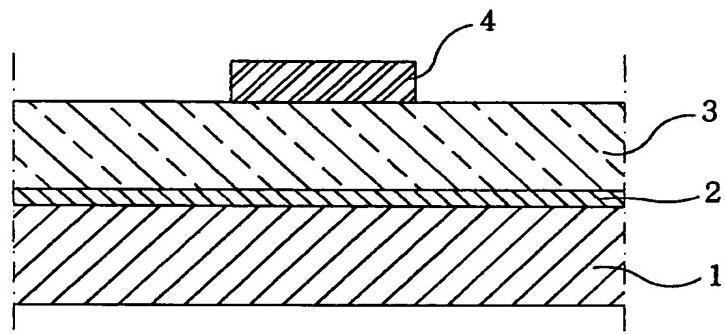
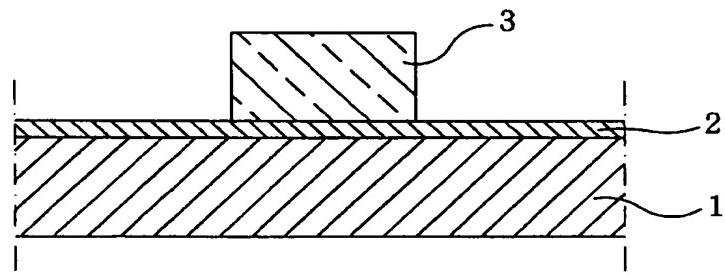
FIG. 1A***FIG. 1B***

FIG. 2A

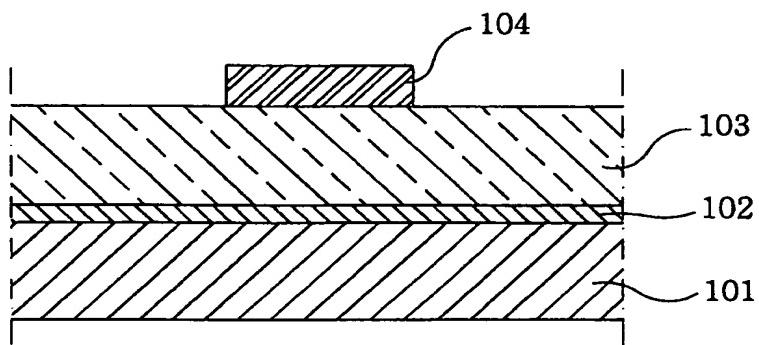


FIG. 2B

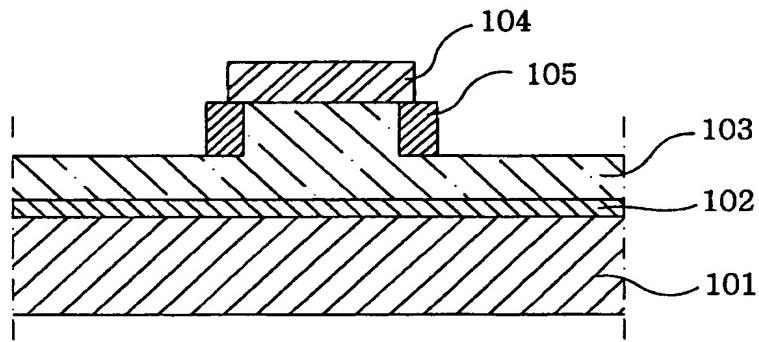
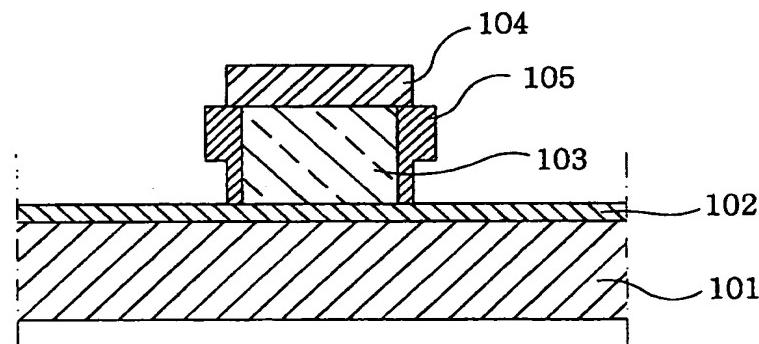


FIG. 2C



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"Methods for Forming a Gate in a Semiconductor Device"

FIGS. 2D-2E

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FIG. 2D

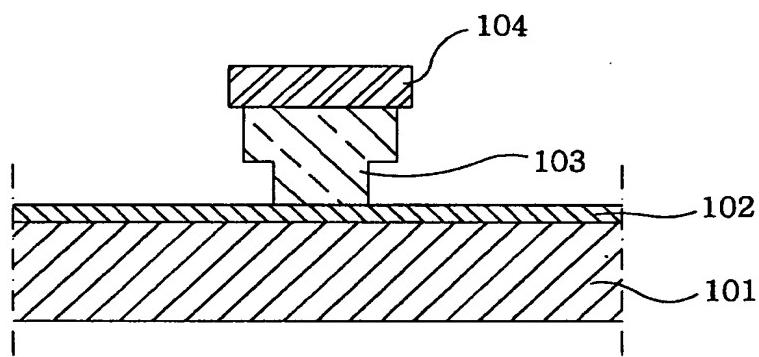


FIG. 2E

